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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10 / 644210
		Filing Date	8-20-2003
		First Named Inventor	Rivers
		Art Unit	2189
		Examiner Name	R. Elmore
Sheet 2	of 2	Attorney Docket Number	YOR920030249US1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RIF		M ZHANG AND K. ASANOVICH, Highly Associative Caches for Low-Power Processors, Kool Chips Workshop, 33rd Intl Symposium on Microarchitecture, . Published in: US	
RIE		JUCE A. RIVERS, Performance of High-Bandwidth Multi-Lateral Cache Organizations, 4/1/1998, Page(s) 143-147	

Examiner Signature	R. Elmore	Date Considered	12-7-2005
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

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